

On page 1, line 15, after "component pads" add -- 12--.

On page 1, line 17, after "printed wiring board" add -- 14--.

On page 1, line 17, after "traces" add -- 10--.

On page 2, line 3, replace "As" with --Referring to Fig. 1B, as--.

On page 2, line 4, after "digital signals" add -- 17--.

On page 2, line 5, after "printed circuit board" add -- 14--.

On page 2, line 5, after "various components" add -- 7 and 7a--.

On page 2, line 6, after "destinations" add -- 7a--.

On page 2, line 6, after "input receivers" add -- 19--.

On page 2, line 7, after "example" add --, as shown in Fig. 1C--.

On page 2, line 8, after "control signals" add -- 42--.

On page 2, line 8, after "memory controller" add -- 40--.

On page 2, line 9, after "the memory" add -- 64--.

On page 6, after the description of Fig. 1 on lines 8-9, insert the following lines:

--Fig. 1A is a schematic diagram of a component on a printed circuit board as is in the prior art; --

--Fig. 1B is a block diagram of two components connected on a printed circuit board; --

--Fig. 1C is a block diagram of a memory controller and memory as an example of two components connected on a printed circuit board; --

On page 10, line 15, after "another circuit board" add --43. The DIMM printed circuit board 43 connects to slot 48 via an edge connector 45--.

On page 10, line 20, after "DIMM memory" add --47--.

On page 11, line 1, after "printed wire board" add --43--.

On page 11, line 1, after "signal traces" add --22--.

On page 11, line 2, after "control signals" add --42--.

On page 11, line 2, after "corners of the pads" add --24--.

On page 11, line 4, after "DIMM memory" add --47--.

On page 11, line 5, after "DIMM printed circuit board" add --43--.

On page 11, after line 6, insert new paragraph

--Referring to Fig. 8, the edge connector 45 attaches the DIMM printed circuit board to the printed circuit board containing the memory controller. The DIMM memory components attach to the DIMM printed circuit board. As can be seen from Fig. 8, areas 80-87 have the connect structure of the pads with the traces as described in the invention and as shown in Fig.

43
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IN THE CLAIMS:

Cancel existing Claims 1-11 inclusively. Replace the cancelled claims with the following new Claims 12-36.

Claim 12 (new). The apparatus for a signal-triggered digital circuit, said apparatus comprising:

44

- a signal source for generating a digital signal;
- an input receiver, said input receiver receiving said digital signal for said digital circuit and being responsive to triggering induced by said digital signal;
- a conducting interface;